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| TRANSMITTAL FORM (to be used for all correspondence after initial filing) | Application Number | 09/653,149 |
| | Filing Date | August 31, 2000 |
| | First Named Inventor | Garo J. Derderian |
| | Group Art Unit | 2818 |
| | Examiner Name | T. Lee |
| Total Number of Pages in This Submission | Attorney Docket Number MI22-1330 | |

| ENCLOSURES (check all that apply) | | |
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| <input type="checkbox"/> Fee Transmittal Form | <input type="checkbox"/> Assignment Papers (for an Application) | <input type="checkbox"/> After Allowance Communication to Group |
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| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | The Commissioner is hereby authorized to charge any additional fees required under 37 C.F.R. § 1.16 and § 1.17 and credit any overpayments to account no. 23-0925. | |

| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT | |
|--|---|
| Firm or Individual name | James E. Lake; Reg. No. 44,854; Wells St. John P.S. |
| Signature | |
| Date | 17 Jan 2002 |

| CERTIFICATE OF MAILING | |
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| I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 1/17/2002 | |
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BV 0-6161481



Inventor: Garo J. Derderian, et al
Assignee: Micron Technology, Inc.
Serial No.: 09/653,149
Filing Date: August 31, 2000
Attorney Docket No. MI22-1330
Title: Capacitor Fabrication Methods and Capacitor
Constructions

INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is directed to the documents listed on the attached Form PTO-1449, and copies of which are attached. No admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 17 Jan 2002

Attorney: _____

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